



## PATENT ABSTRACTS OF JAPAN

(11) Publication number: **2000244378 A**(43) Date of publication of application: **08.09.00**

(51) Int. Cl.

**H04B 7/08****H04B 1/707****H04L 7/00**(21) Application number: **11046965**(22) Date of filing: **24.02.99**(71) Applicant: **KOKUSAI ELECTRIC CO LTD**(72) Inventor:  
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SPECTRUM COMMUNICATION,  
DEMODULATING CIRCUIT AND RECEIVER**

part 14 is not present, and the data can directly be transferred from the data memory part 14 to the fast MF 16.

(57) Abstract:

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**PROBLEM TO BE SOLVED:** To reduce the number of components, and to reduce power consumption by storing received signals by data units for at least one symbol, quickly reading the stored data by symbol units, multiplying the data by a quickly read spread code, simultaneously adding them, and outputting a correlated output.

**SOLUTION:** A spread spectrum signal transmitted from a receiving part is processed by a spread code with a prescribed chip time interval, and the sum of product arithmetic operation is quickly processed by using a fast matched filter(MF) 16. The time conversion is executed by storing the spread spectrum signals for at least one symbol in a data memory part 14, and quickly reading them as a result. Thus, the data from an A/D converter 11 can directly be stored in a multiple tap F/F 15, and quickly read from this even when any data memory

